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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant : Yangsung Joo

Application No.: 10/783,976 Confirmation No.: 4392

Filed : February 20, 2004

FOr : DRIVING A DRAM SENSE AMPLIFIER HAVING

LOW THRESHOLD VOLTAGE PMOS TRANSISTORS

Group Art Unit : 2824

Examiner : Pho M. Luu

New York, New York 10020 July 1, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

The above-identified patent application was allowed on June 15, 2005. The Notice of Allowability included a Statement of Reasons for Allowance.

As part of the Statement, the Examiner stated (on page 2 of the Notice of Allowability) that "[t]here is no teaching or suggestion in the prior art" and then went on to set forth features from applicant's claims as quotes. However, the "quotes" are not exact, and to the extent that the language set forth by the Examiner in the form of a quote differs from the actual claim features, applicant respectfully submits that his invention should

be limited only by the claim language itself and not by the inexact quotes that appear in the Statement of Reasons for Allowance.

To assist the Examiner, applicant sets forth in the tables below his claim features as compared to the language "quoted" in the Statement of Reasons for Allowance:

Claim 58	Statement of Reasons for
	Allowance
circuitry operative to raise	the circuitry operative to
said input terminal of said	raise the input terminal of
amplifier to said full	the amplifier to the full
supply voltage after said	supply voltage after the
differential voltage	differential voltage
develops between said pair	develops between the pair of
of signal lines	signal line in an electrical
	circuit

Claim 69	Statement of Reasons for Allowance
raising said input terminal of said amplifier to a full supply voltage after said differential voltage develops between said pair of signal lines	the circuitry operative to raise the input terminal of the amplifier to the full supply voltage after the differential voltage develops between the pair of signal line in an electrical circuit

Claim 80	Statement of Reasons for
	Allowance
terminal of said amplifier to a full supply voltage after said differential voltage develops between	the circuitry operative to raise the input terminal of the amplifier to the full supply voltage after the differential voltage develops between the pair of signal line in an electrical circuit

Applicant respectfully invites the Examiner to issue a revised Statement of Reasons for Allowance using exact quotes from the claims.

Respectfully Submitted,

Evely C. Mal

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